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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Page **1** of **2**

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Application Number	09/805,597
Filing Date	March 12, 2001
First Named Inventor	Estacio
Art Unit	2822
Examiner Name	Monica Lewis
Attorney Docket Number	018865-005800US

U.S. PATENT DOCUMENTS

Examiner	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number Kind Code ² (if known)			
ML	AA	US-5,105,536	04-21-1992	Neugebauer, et al.	
	AB	US-6,198,163 B1	03-06-2001	Crowley, et al.	
	AC	US-6,215,176 B1	04-10-2001	Huang	
	AD	US-6,399,418 B1	06-04-2002	Glenn, et al.	
	AE	US-6,452,278 B1	09-17-2002	DiCaprio, et al.	
	AF	US- 09/464,885	12-16-1999	Joshi, et al.	
	AG	US-2003/0025183	2-6-2003	Thornton et al.	
	AH	US-			
	AI	US-			
	AJ	US-			
	AK	US-			
	AL	US-			
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FOREIGN PATENT DOCUMENTS

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		Country Code ³	Number ⁴	Kind Code ⁵ (if known)				
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PTO/SB/08B (04-03)

Approved for use through 04/30/2003. OMB 0651-0031

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NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
ML	AZ	"IR's New Synchronous Rectifier Chip Set Meets New Efficiency Standard for DC-DC Converters to Power Notebook PC Processors Through 2000," International Rectifier Company Information. Retrieved from the World Wide Web at http://www.irf.com/whats-new/nr990402.html on July 29, 2003.	
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